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| 01.  | **VLSI development Platform (Universal for FPGA & CPLD)**  
      Mother Board Specifications :  
      On board : +5V, 3.3V, 2.5V, 1.8V supply  
      LCD display : 16 X 2 Character LCD  
      7 Segment : 4 digits  
      8 Channel ADC : 8-bit Resolution  
      Push button : 4 ; Output LEDs : 16 ; DIP switches : 16  
      HEX keypad : 1 ; DAC : 8-bit Resolution ;  
      Memory : SRAM 32KB  
      Peripheral Interface : Serial, PS2, VGA, USB etc.  
      FPGA Daughter Card : Spartan 3, XC3S400PQ208  
      Device density : 400k gates ; On board : 8 MHz crystal  
      Master reset Key : For hardware reset  
      On board : Flash EPROM for FPGA boot.  
      Configuration Method : JTAG/Boundary scan interface ; PROM interface  
      CPLD Daughter Card : CPLD, XC95108TQ100  
      Device density : 2400 gates, 108 macro cells.  
      On board : 8 MHz crystal  
      Configuration Method : JTAG interface (boundary scan)  
      Std. Accessories to be supplied with Kit :  
      FPGA & CPLD JTAG Cable , Male to Female serial cable.  
      USB Cable , Power Supply +12 V , Xilinx Web pack CD External DC Power supply with outputs: 0-30V/1A Variable , 0 to ±15V/1A Variable & 5V/1A Fixed with Digital display for Voltage & current . Outputs should be short circuit proof & over load protected. | 03  |